



SDS
NATIONAL DATA SYSTEMS

NATIONAL SOFTWARE SUPPORT MANUAL

RI

*Eliminer
ces memos*

SUBJECT: Table of Contents
and Obsolete NSS Memoranda

IDENTIFICATION: 69-00-02D
DATE ISSUED September 1, 1968
SECURITY
PAGE 1 OF 7

*Pointer sur
la liste page
2 et suivantes
ceux que j'ai.*

The following is a list of NSS Memoranda which are obsolete and should be removed from your NSS Notebook. They will no longer appear on the attached Table of Contents.

- 69-03-01A Benchmark Submittal for Sigma 2, Sigma 5/7
- 69-03-02A Magnetic Tapes Available for Copying
- 69-10-01A Sigma 7 Benchmark Submittal
- 69-14-09A An Example of the Use of File Manage in Sigma 7 BPM
- 69-16-03A Sigma 5/7 Batch Processing Monitor
- 69-16-06A Description - BPM System Generation
- 69-40-02A Sigma 2 Software Sizes
- 69-40-03A Sigma 2 Software Publications
- 69-70-03A Current 9 Series Standard Software
- 69-70-04A Basic Library of SDS Manuals
- 69-70-05A List of Available Special Programs
- 69-70-06A Tape Copy Requests for 9 Series
- 69-73-02A Monarch System Tape Release
- 69-73-05A Monarch System Release
- 69-74-01A Overlay Note
- 69-75-02A 900 Series Real-Time Monitor Overlay Structure
- 69-76-05A Backspacing Binary Records in FORTRAN II and FORTRAN II Real-Time
- 69-90-01A Conversational Compiler System (CCS)
- 69-90-02A Modifying the SDS Time-Sharing System I/O

Attachment
Table of Contents

APPROVED BY	ISSUED BY	IDENT. NO.	REVISION LETTER	DATE PREVIOUS ISSUE
<i>Susanne Klee</i> held Klee	Susanne Froehlich	69-00-02	D	June 20, 1968

IDENTIFICATION: 69-00-02D
 DATE ISSUED September 1, 1968
 SECURITY
 PAGE 2 OF 7

SUBJECT: Table of Contents

<u>SECTION</u>	<u>SUBJECT</u>	<u>ISSUE DATE</u>
69-00-XX	<u>GENERAL REFERENCE</u>	
69-00-00A	NSS Notebook Tabs	5/1/66
69-00-01A	NSS Notebook Label	5/1/66
69-00-02D	Table of Contents	9/1/68
69-00-03A	NSS Notebook Organization	5/8/67
69-00-04A	New Numbering System for National Software Support Memos	5/8/67
69-00-05A	NSS Manual	4/25/67
69-00-06A	NSS Memos (Corrections)	6/20/67
69-02-01B	SIDIR Status Report (Monthly Issue)	8/9/68
69-10-XX	<u>SIGMA 5/7 MONITORS</u>	
69-10-02A	Sigma 5/7 Nomenclature Clarification	3/10/67
69-10-03A	Sigma 5/7 Software Questions From the Field	3/10/67
69-10-04A	Questions From the Field	3/28/67
69-10-05A	Sigma 5/7 DCBs and Operational Labels	10/2/67
69-10-06A	Sigma 5/7 Symbol Error Codes	12/21/67
69-10-07A	Sigma 5/7 Utility Programs	1/19/68
69-10-08A	RAD-75 Overlay Loader for Sigma 5/7	5/10/68
69-10-09A	Sigma 5/7 Symbol SIDIRs	5/27/68
69-10-10A	Sigma 5/7 Software Report	7/5/68
69-12-01A	Sigma 7 Stand-Alone Systems	5/23/67
69-12-02B	Sigma 5/7 BCM/Stand-Alone Common Software Package	1/8/68
69-14-01A	Sigma 7 Basic Control Monitor Modifications	2/1/67
69-14-02A	Sigma 7 Symbol Prefixes	3/2/67
69-14-03A	Sigma 7 System Tape Under BCM	2/25/67
69-14-04B	Load Map - Sigma 5/7 BCM	10/4/67
69-14-05B	Upgrading of Sigma 7 Basic Software to Provide Facilities for the Handling of BCD Data	8/11/67
69-14-06A	System Generation for Sigma 7 BCM	9/11/67
69-14-07A	Basic Control Monitor for Sigma 5/7 JIT	9/11/67
69-14-08A	Devices and Operational Labels in Sigma 7 BCM	9/22/67
69-14-10A	Differences Between RAD-75 and RBM Version A00	6/18/68

cont'd

IDENTIFICATION: 69-00-02D
 DATE ISSUED September 1, 1968

SUBJECT: Table of Contents

SECURITY

PAGE 3 OF 7

<u>SECTION</u>	<u>SUBJECT</u>	<u>ISSUE DATE</u>
69-10-XX	(cont'd)	
✓ 69-16-01A	Batch Monitor Control Card Usage	5/1/67
69-16-02B	Sigma 5/7 Batch Questions	1/29/68
✓ 69-16-04A	An Example of the Use of File Manage in Sigma 7 BPM	10/10/67
69-16-05B (P)	Accounting Information in Sigma 7 BPM	1/8/68
69-16-07A	Sigma 5/7 Program Segmentation for Overlay Under BPM	4/24/68 ?
69-16-08A	Sigma 7 BPM: Use of Console Teletype as C Device	5/16/68
✓ 69-16-09A	Relocatable Object Module Translator (ROMTRAN)	6/20/68
✓ 69-16-10A	BPM System I/O Tables	7/2/68
✓ 69-16-11A	Recovery Procedure for BPM Version B00	7/1/68
✓ 69-16-12A	Sysgen for Minimal BPM System	7/16/1968
69-16-13A	BPM Loader Documentation	8/14/68 ?
✓ 69-16-14A	Basic DEF Process and Monitor Boot	8/15/68
69-20-XX	<u>SIGMA 5/7 FORTRAN, COBOL</u>	
69-20-01A	Converted Sigma 7 FORTRAN IV-H Source Programs to Double Precision	2/22/67
69-20-02A	Differences Between SDS FORTRAN IV and FORTRAN IV-H on Sigma 7	3/22/67
✓ 69-20-03A	Recovery Procedures for Common Errors Encountered When Using FORTRAN IV-H (Sigma 5/7)	4/21/67
✓ 69-20-04B	Sigma 7 FORTRAN IV Language Definitions	3/13/68
69-20-05A	ENCODE/DECODE Facility with Sigma 5/7 FORTRAN IV-H	8/10/67
69-20-06A	Calling Additional Library Functions with FORTRAN IV-H	9/21/67
✓ 69-20-07A	Sigma 5/7 FORTRAN IV Boolean Operations	11/7/67
69-20-08B	FORTRAN IV-H Calling/Receiving Sequences	1/11/68
69-20-09A	Differences Between Sigma 5/7 FORTRAN IV-H and 1108 FORTRAN IV	12/22/67
69-20-10A	How to Perform I/O with Hexadecimal Data in FORTRAN IV-H	1/5/68
69-20-11A	Job Setup for FORTRAN IV-H Programs under the BPM	1/10/68 ?
✓ 69-20-12A	Comparison of Version A.00, B.00 and C.00 FORTRAN IV-H	4/3/68
✓ 69-20-13A	Sigma 5/7 FORTRAN IV Technical Terminology for Library and Generated Code	3/25/68
✓ 69-20-14A	Sigma 5/7 FORTRAN IV Calling and Receiving Sequences at Execution Time	4/25/68
✓ 69-20-15A	FORTRAN IV-H DO Statements	4/9/68
69-20-16B	Conversion of Sigma 5/7 FORTRAN IV-H Programs to SDS FORTRAN IV	7/24/68

cont'd

ATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-00-02D

DATE ISSUED September 1, 1968

SUBJECT: Table of Contents

SECURITY

PAGE 4 OF 7

<u>SECTION</u>	<u>SUBJECT</u>	<u>ISSUE DATE</u>
69-20-XX	(cont'd)	
/ 69-20-17A -	Permanent File Capability for FORTRAN IV-H Users Under BPM	6/10/68
/ 69-20-18A -	FORTRAN Execution Timing Routine	7/16/68
/ 69-24-01A -	Object Code to be Generated by Sigma 5/7 FORTRAN IV	4/25/68
/ 69-24-02B -	Sigma 5/7 Compiler Compatibility with USASI Standards	6/4/68
/ 69-24-03A -	Sigma 5/7 FORTRAN IV Compiler Diagnostic Demo (Model #705296-11A00)	7/5/68
/ 69-24-04A -	Interfacing Assembly Language Routines with SDS Sigma 5/7 FORTRAN IV	8/2/68
69-24-05A -	Sigma 5/7 FORTRAN IV Run-Time Diagnostics	8/14/68
69-26-01C (6) -	Sigma 7/360 COBOL Comparison	6/24/68
/ 69-26-02A	SDS COBOL-65 Sub-Compile Feature	5/29/68
69-30,32-XX	<u>SIGMA 5/7 ASSEMBLERS</u>	
69-30-01A -	Sigma 7 Symbol - A Display of Hexadecimal Representation Produced by the Instructions	3/1/67
/ 69-32-01A -	SDS Sigma Standard Compressed Language	4/2/68
69-33-XX	<u>SIGMA 5/7 BUSINESS SOFTWARE</u>	
69-33-01A -	Sigma 5/7 Mathematical Routines - Average Execution Times	9/20/67
69-33-02A -	RAD Sort/Merge Contingencies and Provisions	4/23/68
69-33-03A	1401 Simulator Operation	8/6/68
69-34,35-XX	<u>SIGMA 5/7 APPLICATIONS</u>	
69-36,37-XX	<u>SIGMA 5/7 HARDWARE</u>	
69-40-XX	<u>SIGMA 2 MONITORS</u>	
/ 69-40-01B	Sigma 2 BCM and Processor Core Sizes	6/5/68
/ 69-40-04B	Sigma 2 Operating Systems	6/7/68
/ 69-40-05B	Sigma 2 Multiply/Divide Simulation.	2/28/68
/ 69-40-06B	Sigma 2 Software Publications	3/13/68
/ 69-40-07B	Sigma 2 BCM Interrupt Priorities	1/9/68
69-40-08A	Sigma 2 Programs for Calcomp Plotters	5/9/68
/ 69-40-09A	Sigma 2 SIU Software	7/8/68

cont'd

ATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-00-02D
 DATE ISSUED September 1, 1968
 SECURITY
 PAGE 5 OF 7

SUBJECT: Table of Contents

<u>SECTION</u>	<u>SUBJECT</u>	<u>ISSUE DATE</u>
69-40-XX (cont'd)		
/ 69-42-01B	Patch Cards for Sigma 2 Stand-Alone Software	2/1/68
/ 69-42-02D	Sigma 2 Stand-Alone Math Library (Single Precision Floating Point)	7/29/68
69-42-03B	Sigma 2 Stand-Alone Software	2/22/68
69-42-04A	Sigma 2 Stand-Alone Symbol Demonstration Programs	12/20/67
/ 69-42-05A	Sigma 2 Stand-Alone RAD Handler	1/5/68
/ 69-42-06A	Debug with Trace	3/11/68
69-42-07A	Sigma 2 Universal Utility Program	5/14/68
/ 69-42-08A	Comparison of Stand-Alone Support System "A" Version versus "B" Version	6/5/68
69-44-01A	Simulation of RBM Background Overlay Feature with BCM for Sigma 2	10/31/67
69-44-02C	Sigma 2 I/O Task Suspension Under BCM	4/26/68
/ 69-44-03B	Sigma 2 BCM System Preparation	4/19/68
/ 69-44-04A	Modifying BCM Operation Label Assignments in Core	3/21/68
69-44-05A	Sigma 2 BCM Operational Label Usage	3/25/68
69-44-06A	Core Minimization Techniques for Sigma 2 BCM Systems	4/17/68
/ 69-44-07A	Sigma 2 BCM as a Tape System	4/18/68
/ 69-44-08B	Sigma 2 Basic Control Monitor "A" Release	6/6/68
/ 69-44-09A	Sigma 2 Real-Time Clock 1 Foreground Demonstration	5/14/68
/ 69-44-10A	Sigma 2 BCM Bootstrap & Absolute Loader Generator	5/16/68
/ 69-44-11A	Sigma 2 Basic Control Monitor Background Demonstration	6/12/68
69-50-XX	<u>SIGMA 2 BASIC FORTRAN</u>	
69-50-01B	Comparison of Sigma 2 Basic FORTRAN and Basic FORTRAN IV with USASI Standards and 1130/1800 FORTRAN	5/3/68
69-50-02A	Sigma 2 Basic FORTRAN Demonstration Programs	3/18/68
69-60-XX	<u>SIGMA 2 SYMBOL</u>	
/ 69-60-01A	Sigma 2 Stand-Alone Symbol, Version A versus Version B Features	6/3/68
69-62-01A	Sigma 2 Extended Symbol versus Symbol Features Comparison	3/12/68
69-66-01B	Sigma 2 Interrupt Programming Restriction	12/9/67
69-66-02B	Sigma 2 I/O Data Chaining with Interrupt	5/6/68

cont'd

OPTIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-00-02D

DATE ISSUED September 1, 1968

SUBJECT: Table of Contents

SECURITY

PAGE 6 OF 7

<u>SECTION</u>	<u>SUBJECT</u>	<u>ISSUE DATE</u>
69-70-XX	<u>9 SERIES AP ANALYSTS REFERENCE</u>	
69-70-01A	900 Series Course Outline with Complete References	10/31/66
69-70-02A	Basic Programmed Operator Data (910/928)	12/10/66
69-73-01A	Example of Adding Routines to the Monarch System Tape Using Four Magnetic Tapes but no Punch Output	9/9/66
69-73-03A	Monarch Operating System	12/13/66
69-73-04A	Note on Adding a Control Message to Monarch	1/26/67
69-73-06A	Additions to 900 RAD Monarch	2/1/68
69-73-07A	Assignment Constraints in Symbol Under Tape Monarch	2/1/68
69-74-02A	Updating 9300 Monitor with a Simple Meta-Symbol Extension	7/15/66
69-74-03A	Changing the Standard Peripheral Assignments in the 9300 Monitor	1/25/67
69-74-04A	9300 Monitor Overlay	7/26/66
69-74-05A	9300 Monitor Snaps, Dumps, Patches	8/12/66
69-74-06A	9300 Monitor - Version AD	9/2/66
69-74-07A	Note on Adding a Control Message to the 9300 Monitor	2/10/67
69-74-08A	9300 Monitor System - Version AE-1	5/15/67
69-74-09A	Name List Philosophy in 9300 Monitor Overlay	10/3/67
69-74-10A	9300 Monitor - A00 Release	2/27/68
69-74-11A	Contents of 9300 A00 Relocatable Binary Tape	3/12/68
69-74-12A	Interface of Manage with 9300 Monitor	4/10/68
69-74-13A	9300 A00 Monitor and the Card Punch	5/27/68
69-74-14A	9300 Monitor - Symbolic Changes AE-1 to A00	5/27/68
69-74-15A	9300 Monitor Format (AE-1 to A00)	6/3/68
69-75-01A	900 Real-Time Monitor: Timing of Reentrance Monitor	6/1/67
69-75-03B	Real-Time Monitor System Generation	1/25/68
69-75-04A	Release of 9 Series & 9300 RTM & Procedures	5/3/68
69-76-01A	900 Series FORTRAN II Compiler Dump	9/1/66
69-76-02A	900 Series FORTRAN II Basic Mag Tape Editor (C Version)	9/1/66
69-76-03A	900 Series FORTRAN Label Trace/Linking	12/10/66
69-76-04A	FORTTRAN II; A Note on Determining Memory Size While Running under Monarch	2/3/67
69-76-06B	900 Series FORTRAN II and Real-Time Fortran II under Monarch	3/28/68
69-76-07A	A00 FORTRAN II Run-Time Tape Assignments	5/2/68

cont'd

ATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-00-02D
 DATE ISSUED September 1, 1968
 SECURITY
 PAGE 7 OF 7

SUBJECT: Table of Contents

<u>SECTION</u>	<u>SUBJECT</u>	<u>ISSUE DATE</u>
69-70-XX (cont'd)		
69-78-01A	9300 FORTRAN IV Object Time Diagnostic Demo	7/29/66
69-78-02A	9300 FORTRAN IV Object Listing Demo	8/24/66
69-78-03A	9300 FORTRAN IV Object Code Demo	10/21/66
69-78-04A	9300 FORTRAN IV: Parts and Pieces	10/31/66
69-78-05A	More 9300 FORTRAN IV Features	4/3/67
69-78-06B	9300 FORTRAN IV Compiler Diagnostics Demo	5/27/68
69-79-01A	The 9300 Real-Time FORTRAN IV Library	10/14/66
69-79-02A	Interface Between R.T. FORTRAN II Routines and Symbol/Meta-Symbol Subroutines	10/30/67
69-82-XX	<u>9 SERIES META-SYMBOL</u>	
69-82-01A	One Example of How to Extend 9300 Meta-Symbol	7/15/66
69-82-02A	9300 Meta-Symbol Limitations and Speculations	3/29/67
69-82-03A	Developing Meta-Symbol Procs for 900 Series	6/1/67
69-82-04A	9 Series Meta-Symbol Encoded Format	5/1/68
69-83-01A	Corrections to SDS Manage Reference Manual	8/9/67
69-83-02A	FICA & SDI Deduction Calculation in the Payroll Generator.	11/28/67
69-87-01A	Programming the 9367 RAD Files	5/5/66
69-87-02A	Programming the 9164/9165 Disc File	10/25/67
69-90-XX	<u>940 AP ANALYSTS REFERENCE</u>	
69-90-03A	940 Time-Sharing System - 2.0 Performance	5/27/68
69-90-04A	Addendum to 940 BASIC Reference Manual (90 11 11B)	7/8/68
69-90-05A	TSS 940 Peripheral Device Capabilities	7/8/68
69-90-06A	Terminal User's Guide to 940 TSS - 3.0	7/30/68
69-90-07A	Summary of SDS 940 TSS - 3.0 Features	7/31/68
69-90-08A	Technical Guide to 940 TSS - 3.0	7/31/68
69-90-09A	TSS - 3.0 FORTRAN II Changes	8/2/68
69-90-10A	Installation of TSS - 3.0	7/31/68
69-90-11A	940 SIDIR Status Summary as of TSS - 3.0 Release 7/31/1968	7/31/68



SCIENTIFIC DATA SYSTEMS

NATIONAL SOFTWARE SUPPORT MANUAL

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SUBJECT: SIGMA 5/7 BCM/STAND-ALONE
COMMON SOFTWARE PACKAGE

SECURITY

PAGE 1 OF 5

The "Sigma 5/7 BCM/Stand-Alone Common Software Package", catalog number 704127 - 24A00, is a library of relocatable binary programs from which absolute binary programs may be generated.

The absolute binary programs which can be formed are:

1. BCM
2. Stand-Alone ABS Dump Loader with I/O handlers
3. Stand-Alone Loader with I/O handlers

A. In order to generate the above absolute binary programs, procede in the following manner:

1. Obtain the following SDS program or comparable programs:

Sigma 5/7 Absolute Bootstrap Program
(704145-24, or -23)

Sigma 5/7 Stand-Alone Loader with I/O handlers
(704142-84, or -83)

Sigma 5/7 Stand-Alone ABS Dumping Loader with I/O handlers
(704155-84, or -83)

Sigma 5/7 Stand-Alone Symbol Assembler
(704160-84, or -83)

Sigma 5/7 BCM/Stand-Alone Common Software Package
(704127-24, or -23)

2. Generate a relocatable binary deck which contains primary external references to each SDS common software module desired. The primary external references are of the form CNXXXXXX where XXXXXX is a six character SDS catalog number. The Stand-Alone Symbol Assembler may be used to generate the relocatable object module. (See NSS 69-12-03A)

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Sheldon Klee	<i>Marida Slobko</i> Marida Slobko	69-12-02	B	October 5, 1967

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SUBJECT: SIGMA 5/7 BCM/STAND-ALONE
COMMON SOFTWARE PACKAGE

SECURITY

PAGE 2 OF 5

3. Generate an ABS Boot Loader. The ABS Boot Program (704145) may be used to produce an ABS Boot Loader on the BO device. (See NSS 69-14-06A, pg. 2)
4. Generate the desired ABS binary program. This may be accomplished with the Sigma 5/7 Stand-Alone ABS Dump Loader with I/O handlers. Assign the relocatable binary module produced by Step 2 above to BI and the Sigma 5/7 BCM/Stand-Alone Common Software Package to LI (Library Mode). The output of ABS Dump Loader to the BO device will follow the ABS Bootstrap Loader produced in Step 3. (For sample deck set-up, see appendix I)
 - a. Recommended Relocation Bias for use in forming various ABS decks:

BCM: Bias must be $\geq 60_{16}$. If no external interrupts are to be used, 60_{16} may be used. If external interrupts are used, the bias should be the first doubleword boundary above the last interrupt.

Processors (SYMBOL, FORTRAN, LOADER):
Bias should be the first page boundary above TOPMON as listed in the map output when BCM was formed (i.e. 7 pages, $0E00_{16}$).
 - b. Recommended Relocation Bias for use in forming various Stand-Alone ABS decks:

S.A. Loader with I/O handler, Catalog No. 704142,
S.A. ABS Dump Loader with I/O handlers, Catalog No. 704155:

 - 1) Stand-Alone I/O handlers: Bias must be $\geq 60_{16}$.
 - 2) Stand-Alone Loader, Catalog No. 704141:
Bias must be the first doubleword boundary above the value for HIGHEST LOC obtained from the map produced by loading S.A. I/O handlers.
 - 3) S.A. ABS Dump Loader, Catalog No. 704154:
(Same as 2).

NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-12-02B

DATE ISSUED January 8, 1968

SUBJECT: SIGMA 5/7 BCM/STAND-ALONE
COMMON SOFTWARE PACKAGE

SECURITY

PAGE 3 OF 5

- B. Binary programs included in the Sigma 5/7 BCM/Stand-Alone Common Software Package (relocatable binary deck*) are ordered as indicated below.

<u>Cat. No.</u>	<u>Title</u>
704133	Sigma 5/7 Monitor for BCM
704367	Sigma 5/7 Stand-Alone I/O Control Program (SALIO)
704368	Sigma 5/7 I/O Handler Interface
704852	Sigma 5/7 FORTRAN BCD Conversion
704854	Sigma 5/7 Dummy FORTRAN BCD Conversion
704851	Sigma 5/7 7-Track Magnetic Tape Handler
704369	Sigma 5/7 Mag Tape I/O Handler
704370	Sigma 5/7 Line Printer I/O Handler
704371	Sigma 5/7 Card Reader I/O Handler
704372	Sigma 5/7 Card Punch I/O Handler
704373	Sigma 5/7 Typewriter I/O Handler
704374	Sigma 5/7 Paper Tape I/O Handler
704363	Sigma 5/7 Ft. Pt. Inst. Simulator (BCM)
704364	Sigma 5/7 Decimal Inst. Simulator (BCM)
704365	Sigma 5/7 Byte Inst. Simulator (BCM)
704366	Sigma 5/7 Convert Inst. Simulator (BCM)
704149	Sigma 5/7 Ft. Pt. Inst. Simulator (SA)
704150	Sigma 5/7 Decimal Inst. Simulator (SA)
704151	Sigma 5/7 Byte Inst. Simulator (SA)
704152	Sigma 5/7 Convert Inst. Simulator (SA)
704153	Sigma 5/7 Unimplemented Instruction Trap Handler (SA)
704131	Sigma 5/7 Initialization Package for BCM
704853	Sigma 5/7 Stand-Alone I/O Initialization
704141	Sigma 5/7 Stand-Alone Loader
704154	Sigma 5/7 Stand-Alone ABS Dump Loader

*This consists of one relocatable binary card deck (704127-84) or two relocatable binary paper tapes (704127-83, 1 of 2, 2 of 2). The 2 of 2 paper tape contains the Sigma 5/7 Stand-Alone Loader (704141-23) and the Sigma 5/7 Stand-Alone ABS Dump Loader (704154-23).

NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-12-02 B

DATE ISSUED January 8, 1968

SUBJECT: SIGMA 5/7 BCM/STAND-ALONE
COMMON SOFTWARE PACKAGE

SECURITY

PAGE 4 OF 5

C. General comments on selection of I/O handlers:

1. A primary external reference must be generated for the I/O handler interface (704368) module.
2. A primary external reference must be generated for either the FORTRAN BCD Conversion or the Dummy FORTRAN BCD Conversion Module.
3. If the 7-track magnetic tape handler is desired, a primary external reference must be generated for the magnetic tape I/O handler as well as one for the 7-track magnetic tape handler.

D. Examples:

1. To form a bootable copy of BCM with line printer, card reader, 7/9 track mag tape, typewriter, FORTRAN BCD Conversion, and floating point simulator, the following is necessary:
 - a. Assemble the following program:
REF CN704133, CN704368, CN704852, CN704851,
REF CN704369, CN704370, CN704371, CN704373,
REF CN704363, CN704131
END
 - b. Execute the ABS Boot Program.
 - c. Load the above relocatable object module using the ABS Dump Loader. (See appendix I.)
2. To form a bootable copy of the Stand-Alone Loader with I/O handlers for typewriter and card reader, the following is necessary:
 - a. Assemble the following programs:
REF CN704367, CN704368, CN704371, CN704373
REF CN704854, CN704853
END
REF CN704141
END

NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-12-02 B

DATE ISSUED January 8, 1968

SUBJECT: SIGMA 5/7 BCM/STAND-ALONE
COMMON SOFTWARE PACKAGE

SECURITY

PAGE 5 OF 5

D. (Cont'd)

2.

b. Execute the ABS boot program.

c. Load the first relocatable object module using the ABS
Dump Loader. (See appendix I.)

d. Load the second relocatable object module above by
repeating Step "c".

1 Attachment

MS/sf

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SUBJECT: SIGMA 5/7 BCM/STAND-ALONE
COMMON SOFTWARE PACKAGE

SECURITY

PAGE 1 OF 1

APPENDIX I - Use of Sigma 5/7 Stand-Alone ABS Dump Loader (704155-84)

Deck Setup

	< Sigma 5/7 Stand-Alone ABS Dump Loader >
Deck will be loaded un- conditionally	{ < Symbol program with REF's to catalog numbers to be used > !EOD !EOD
Treated as library - only decks referenced by Symbol Prog. will be loaded.	

Operating Instructions:

1. Boot in loader from card reader
(or paper tape reader)
2. After loader is read, the select light on
teletype will be turned on.
Type in load command:
!L MAP, RB060 (NL) (See SDS Sigma 5/7 Stand-Alone
Systems Operations Manual, 901053.)
3. When entire deck has been read, the select light on
the teletype will be turned on.
Type in run command:
!R ^ (NL)
4. Absolute binary deck will be output on BO device.

69-14-01A
7 February 1967

SUBJECT: SIGMA 7 Basic Control Monitor Modifications
FROM: Jim Gaines
TO: T Distribution

A number of modifications have been made to the Basic Control Monitor, BCM Loader, BCM Symbol and Free Standing Symbol. These are described in this memo.

Jim
Jim Gaines

JG:jp



NATIONAL SOFTWARE SUPPORT MANUAL

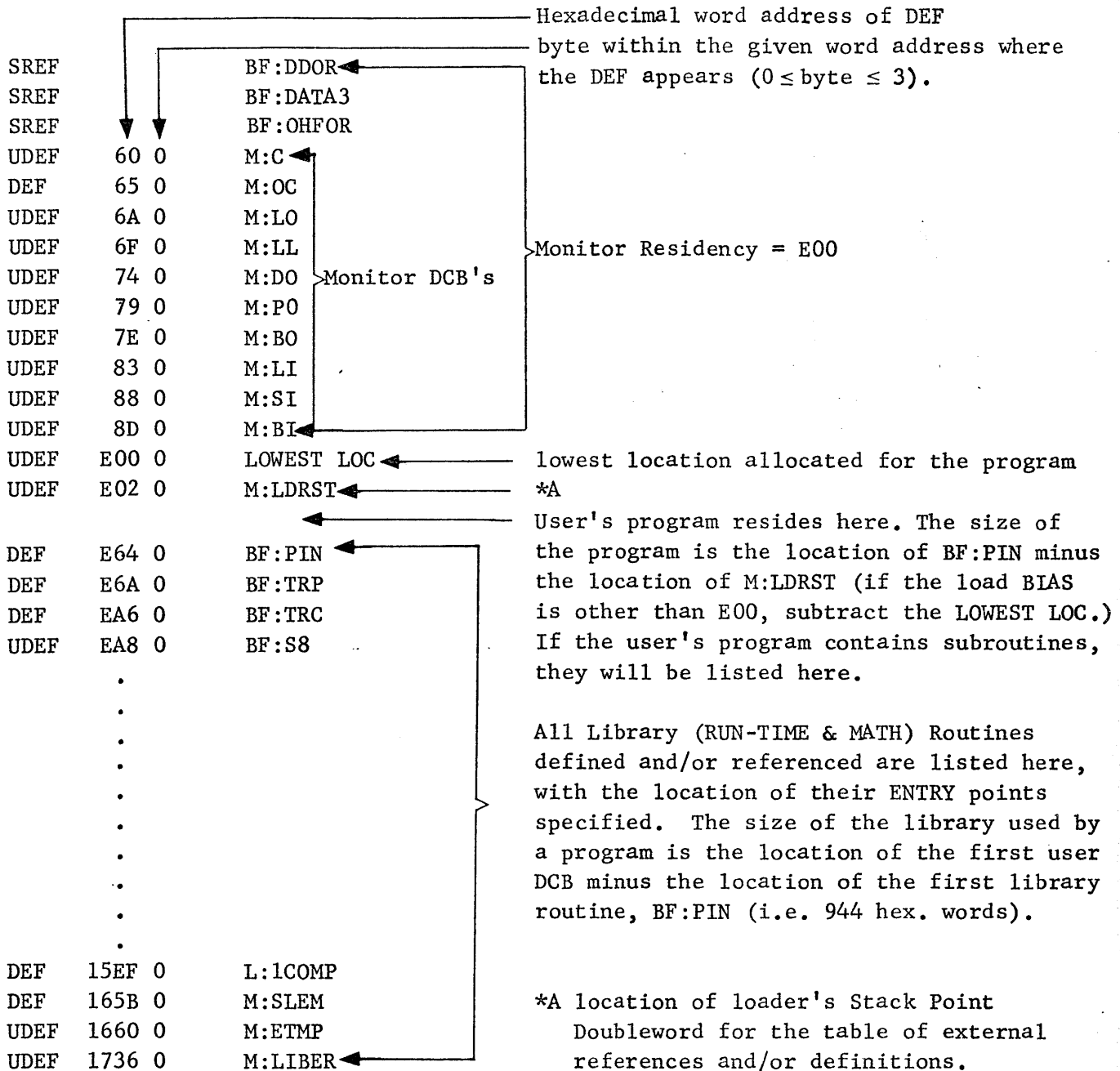
SCIENTIFIC DATA SYSTEMS

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SUBJECT: LOAD MAP - SIGMA 5/7 BCM

SECURITY
PAGE 1 OF 4

The following is a description of a Sigma 7 load map and how it is interpreted.



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NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-14-04B

DATE ISSUED 4 October 1967

SUBJECT: LOAD MAP - SIGMA 5/7 BCM

SECURITY

PAGE 2 OF 4

DEF	17A8	0	F:101	←	User's DCB's
DEF	17AD	0	F:102	←	
DEF	17B2	0	F:103	←	
DEF	17B7	0	F:104	←	
DEF	17BC	0	F:105	←	
DEF	17C1	0	F:106	←	
DEF	17C6	0	F:108	←	
UDEF	17CB	0	F:1	←	
UDEF	17D0	0	F:3	←	
UDEF	17D5	0	F:5	←	
UDEF	17DA	0	F:6	←	
UDEF	1812	0	HIGHEST LOC	←	highest location allocated for the program
				←	Task Control Block
				←	User's Temp Stack
UDEF	223F	0	F4:COM	←	location of beginning of BLANK common. The size of blank common in the upper limit of core minus the location of F4:COM (i.e. 7FFF -- 223F = 5DC0 hex. words).

IDENTIFICATION: 69-14-04B

DATE ISSUED 4 October 1967

SUBJECT: LOAD MAP - SIGMA 5/7 BCM

SECURITY

PAGE 4 OF 4

B. Loader Error Messages (Cont'd)

1. xx = A2 no main program
2. xx = A3 error severity level exceeded (perhaps as result of loading an object module which corresponds to a source deck with a diagnostic).
3. xx = A4 program exceeded core during load -- the temp stacks and program have met.
4. xx = A5 sequence error
nn = sequence number of card which had the error.
5. xx = A6 checksum error.
nn = sequence number of card which had the error.
6. xx = A7 this error message will be given for any of the following errors:
 - a. The first program in user's deck to be loaded did not contain largest blank COMMON reference.
 - b. !EOD card encountered when binary deck expected.
 - c. Compiler error in generating object module (or deck off-punched).
7. xx = A8 illegal load address caused by giving a BIAS (option on !LOAD card) which is not within limits of memory or trying to load a program which references areas outside memory limits.
8. xx = A9 TSS (Temporary Storage Stack) too large -- overlaps COMMON when building TCB.
9. xx = AA compiler error in creation of object module (or deck off-punched).

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IDENTIFICATION: 69-14- 07A
 DATE ISSUED 11 September 1967
 SECURITY
 PAGE 1 OF 4

SUBJECT: BASIC CONTROL MONITOR FOR SIGMA
 5 and 7 - JOB INFORMATION TABLE
 (JIT)

A basic part of the Basic Control Monitor for Sigma 5/7 is the Job Information Table (JIT). This table is used by the monitor for storage of information pertinent to control of background - or foreground-program execution.

This memorandum outlines, in detail, each entry in the JIT and is intended to serve systems programmers as technical documentation supporting BCM in lieu of a monitor technical reference manual.

The JIT for background program execution requires seven words of resident monitor storage. The monitor utilizes a trap location that is, by definition, unused by the Sigma 5/7 hardware for storage of a pointer (right-justified word address) to the first word of the table; this location is '4F'. Each foreground program must supply it's own JIT of seven words, initialized to zero. Upon entry, the foreground program alters the JIT pointer ('4F') to refer to the foreground JIT. The monitor JIT pointer must then be restored before the foreground program exits to the monitor.

The format of the JIT is shown below. The names of the various fields generally correspond to those in the BCM symbolic listing.

WORD 0	ABC	J	D	XSL	ERO	
	0	8	9	10	15	31
WORD 1	RNST	PUF		ABO	CCBEF	
	0	8	9	13	15	31
WORD 2		SL		TSS		
	0	8		15		31
WORD 3		TRFLG		PMDBUF		
	0	8		15		31
WORD 4				TRPLOC		
	0			15		31
WORD 5				ULIMLOC		
	0			15		31
WORD 6				ULIMULOC		
	0			15		31

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NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-14-07A
DATE ISSUED 11 September 1967
SECURITY
PAGE 2 OF 4

SUBJECT: BASIC CONTROL MONITOR FOR SIGMA
5 and 7 - JOB INFORMATION TABLE
(JIT)

Each field of the JIT is defined as follows:

- ABC - I/O Error ABORT Code: Two hexadecimal digits defining the I/O error that has occurred; BCM reference manual 90 09 53A, Pg. 9, Table 6.
- J - Missing Job Flag: This bit is set (1) when a missing JOB control command error (code 60) occurs in order to suppress redundant error print out while searching the C device for the next JOB card. The flag is reset (0) when the next JOB card is encountered.
- D - Data Flag: This bit is set (1) when a data card (non-control command) is encountered when reading the C device. This condition results in a monitor error print out (code 63) and causes the C device to then be read for the next JOB card. This flag is set in order to suppress repetitive error print out in the event several additional data cards are encountered while reading the C device; the flag is reset (0) when the next JOB card is processed.
- XSL - Execution Severity Level: The loader severity level that is to be tolerated by the monitor in accepting a program for execution. This information exists as an option of the RUN control command.
NOTE: One should be aware that although XSL is retained in the JIT, BCM will still allow execution regardless of the entry.
- ERO - Error Override: Temporary storage of the error return address specified in the FPT when an I/O request is made to the monitor.
- RNST - Run Status: A two digit hexadecimal code specifying the reason the program was aborted; BCM reference manual, Pg. 10.
- CCBEF - Control Command Buffer Flag: This flag is set (1) when the Control Command Buffer is full; it is reset (0) when the buffer is empty. The monitor double-buffers the read from the C device.
- PUF - Processor/User Flag: One hexadecimal digit defining who is running; BCM reference manual 90 09 53A, Pg. 10.

NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-14-07A

DATE ISSUED 11 September 1967

SUBJECT: BASIC CONTROL MONITOR FOR SIGMA
5 and 7 - JOB INFORMATION TABLE
(JIT)

SECURITY

PAGE 3 OF 4

- ABO - Abnormal Override: Temporary storage of the abnormal return address specified in the FPT when an I/O request is made to the monitor.
- SL - Severity Level: The severity level (read from the LOAD card) that is to be tolerated by the loader in accepting a relocatable object module.
- TSS - Temporary Stack Size: Size, in words, of the user program temporary stack formed by the loader. The stack pointer double-word is part of the user's task control block.
- NOTE: Originally the monitor was responsible for processing all the options in the LOAD control card. This task is now performed by the loader, thus this entry is no longer used by the monitor.
- TRFLG - Trap Flags: A right-justified field, each bit corresponding to a specified user trap set-reset state. This field is altered by the user trap control function entry to the monitor; BCM reference manual 90 09 53A, Pg. 21.
- PMDBUF - Dump Buffer: The address (word resolution) of a 50 word block of memory, reserved by the loader, immediately following the user program for use by the monitor as: a) a 30 word buffer used to honor requests for snap-shot and post-mortem memory dumps, and; b) a 20 word word buffer used for temporary storage by the monitor in the process of servicing a call for the trap control function; BCM reference manual 90 09 53A, Pg. 21.
- TRPLOC - Trap Location: The address (word resolution) of the entry point to the user's trap routine to be taken if a specified trap occurs. This location is set during the trap control function call to the monitor.
- ULIMLOC - User Lower Limit The word address defining the lower memory limit of background activity; all memory up to this limit is considered to be monitor residence. This limit is calculated during execution of the monitor initialization routine as the location of the next page (1 page = 512 words) boundary following the last word of the resident monitor. The last word +1 of the resident monitor is expressed symbolically as 'TOPMON'. This location is an external DEF and will, therefore, appear on the load map when an ABS deck of the monitor is created using the dumping loader.

NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-14-07A
DATE ISSUED 11 September 1967
SECURITY
PAGE 4 OF 4

SUBJECT: BASIC CONTROL MONITOR FOR SIGMA
5 and 7 - JOB INFORMATION TABLE
(JIT)

ULIMULOC - User Upper Limit: The word address defining the highest memory limit of background activity; usually the last location in the object machine. This limit is calculated during execution of the monitor initialization routine as the last memory location minus the number of pages reserved for the non-resident foreground area.

NOTE: BCM, during initialization after being booted into memory, will determine the memory limit of any Sigma 5/7 by adding 1000_{16} to a word (in the first 4K) and repetitively executing an indirect memory fetch through this word until a trap to '40' occurs. Location '40' had previously been loaded with the appropriate XPSD to return control to the initialization routine which then knows the memory limit.

Miscellaneous Notes

The JIT is maintained entirely by the monitor. Except where noted, each field of the JIT is reset when a JOB (or FIN) control command is read.

The monitor manipulates non-byte fields of the JIT through the use of Load/Store selective instructions. Bits are tested by means of comparing the word to a bit-mask and branching if the AND is (or is not) zero. This technique would allow one to make use of any unused bit positions in the table without affecting current monitor operation.

Monitor routines expect the pointer to the JIT to be in R5 (general register 5) upon entry.

IDENTIFICATION: 69-14-08A
 DATE ISSUED 22 September 1967
 SECURITY
 PAGE 1 OF 6

SUBJECT: DEVICES AND OPERATIONAL LABELS
 IN SIGMA 7 BCM

In lieu of a BCM technical manual, this note documents the structure and function of the key device and operational label tables.

Essentially, there exist two sets of tables, the operational label tables (OPLBT) and the device tables (DCT). OPLBT points to DCT. DCT points to the handlers and the current DCB.

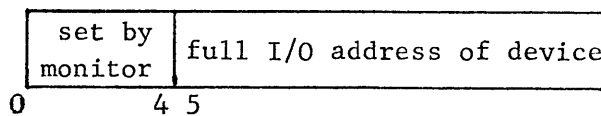
I. DCT Tables

The DCT tables include one entry per physical device. They are parallel in that all the i^{th} entries correspond, although the tables themselves are of different resolutions (i.e., byte, half-word etc.).

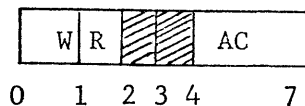
The symbols TYN, PRN, PPN, ... are displacements into the tables pointing to the first entry for that device type. Thus, MTN is a displacement into the DCT tables pointing to the first mag tape unit.

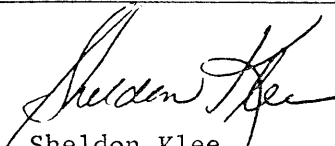
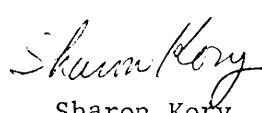
The DCT's marked with an asterisk are the ones established at assembly time and may, therefore, be reassembled to extend the hardware configuration. The remaining DCT's are dynamic -- they are set and used by the monitor and/or the IO handlers.

* DCT1 A half-word table of device addresses.



* DCT3 A byte table of permissible functions (i.e. read/write) and accounting type.



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NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-14-08A
DATE ISSUED 22 September 1967
SECURITY
PAGE 2 OF 6

SUBJECT: DEVICES AND OPERATIONAL LABELS
IN SIGMA 7 BCM

where, W=1 for write permitted
R=1 for read permitted
AC a classification code which may be
included in an accounting routine.

* DCT4 A byte table of device types. The device "type" is actually a displacement into the table of I/O handler addresses (IODEF1).

DCT7 A half-word table of I/O interrupt return addresses. Set by handler.

* DCT9 A half-word table of command pair addresses.

DCT10 A word table of DCB addresses and functions. Set by monitor.

In addition, there are two tables with one entry per device type.

*IODEF1 A half-word table of I/O handler addresses.

*DVLTB A half-word EBCDIC table of device names. The position in this table is also the device type. Thus, the card reader is type 4.

DCT tables continued...

NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-14-08A
 DATE ISSUED 22 September 1967
 SECURITY
 PAGE 3 OF 6

SUBJECT: DEVICES AND OPERATIONAL LABELS
 IN SIGMA 7 BCM

DCT tables (as they exist in BCM - "B" version)

	DCT1	DCT3	DCT4	DVLTB	IODEF1
	0	C1	0	'NO'	0
TYN →	1	C1	1	'TY'	KBTIO
PRN →	5	40	2	'PR'	PTAP
PPN →	5	81	3	'PP'	PTAP
CRN →	3	40	4	'CR'	CRDIN
CPN →	4	81	5	'CP'	CRDOUT
LPN →	2	83	6	'LP'	PRTOUT
MTN →	80	C4	8	'DC'	DISCIO
	81	C4	8	'9T'	MTAP
	:	:	:	'7T'	MTAP
	:	:	:	'MT'	
	87	.	8		
	C0	.	9		
	C1	.	9		
	:	:	:		
	:	:	:		
	C7	C4	9		

Example:

If in a SYST, ASSIGN or FPT, '7TAC2' has been mentioned, the device is "verified" in the following way. '7T' is a valid device name (DVLTB). AC2 (IOP0, device C2) is a valid device address since it exists in the DCT1 table (17th entry). The device can be read and written (17th entry in DCT3). The handler address can be found by looking at the 17th entry in DCT4, which says that the handler for AC2 is the 9th entry in IODEF1.

II. OPLBT Tables

The OPLB tables link the label to the device. Like the DCT's, they are also parallel.

OPLBT1 A half-word text table of labels

NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-14-08A
DATE ISSUED 22 September 1967
SECURITY
PAGE 4 OF 6

SUBJECT: DEVICES AND OPERATIONAL LABELS
IN SIGMA 7 BCM

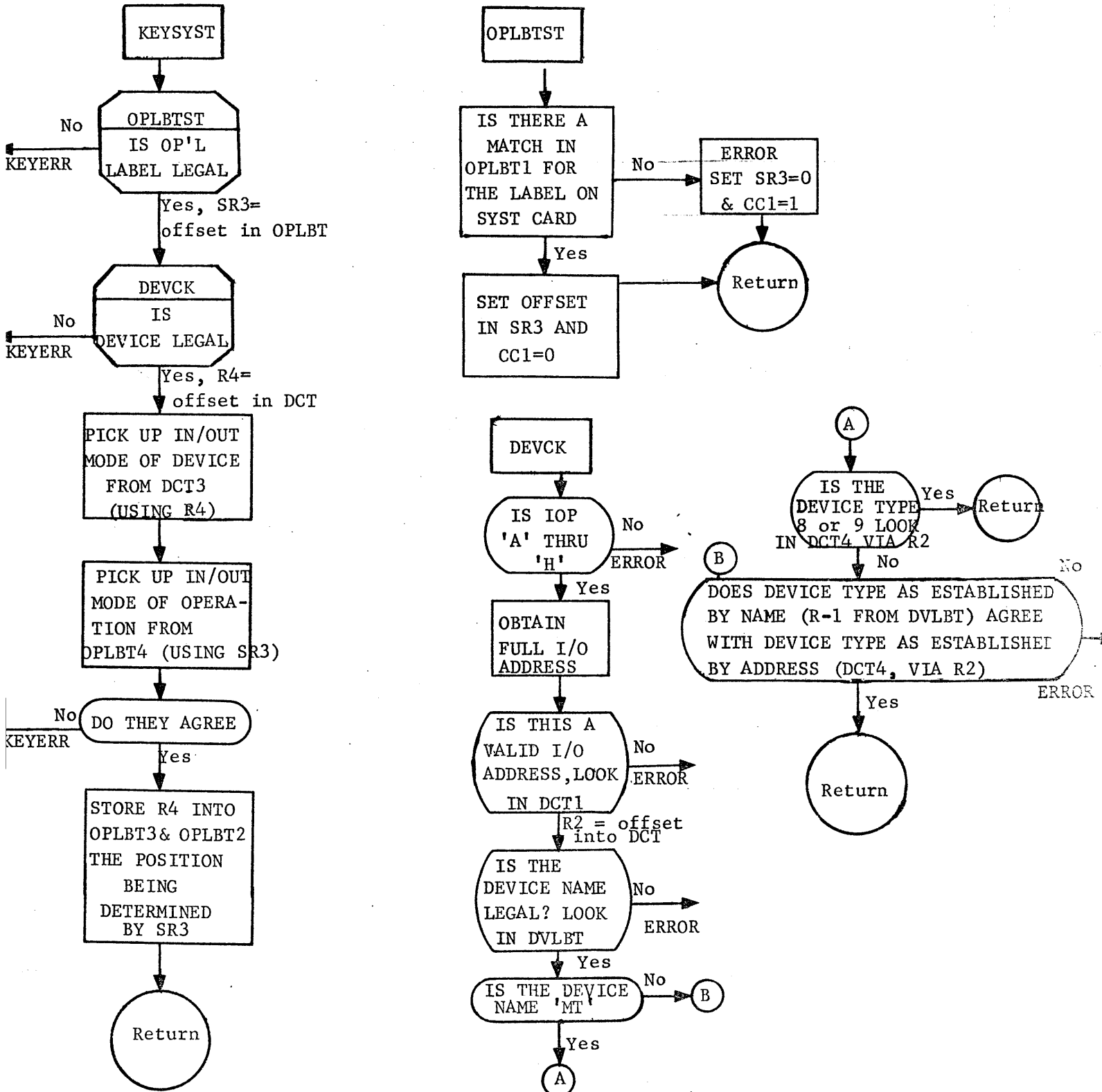
- OPLBT2 A byte table of displacements into DCT. An entry in this table is the "current" operational label assignment to the device.
- OPLBT3 Same as OPLBT3 except that it is "permanent".
- OPLBT4 A byte table of permissible functions. Format is the same as DCT3.

When a !JOB card is read, OPLBT3 is copied into OPLBT2. A !SYST card changes the appropriate entry in OPLBT2 and OPLBT2. In BCM these are the only control cards which affect the OPLBT tables. (The !ASSIGN causes a change in the DCB only.) Effectively, OPLBT2 is redundant in BCM, since it never differs from OPLBT3. (In BPM, this is not the case.)

OPLB Tables (As they exist in BCM - "B" Version)

<u>OPBLBT1</u>	<u>OPLBT2 (Current)</u> <u>(Pointers to DCT)</u>	<u>OPLBT3</u> <u>(Permanent)</u>	<u>OPLBT4</u>
'NO'	0	0	C4
'C'	CRN		40
'OC'	TYN	SAME	C2
'LO'	LPN		83
'LL'	LPN	AS	83
'DO'	CPN		82
'PO'	CPN	OPLBT2	81
'BO'	CPN		81
'LI'	CRN		40
'SI'	CRN		40
'BI'	CRN		40

III. !SYST Card. A quick picture of the relationship between DCT and OPLBT can be gained by looking at the monitor response to the !SYST card.



NATIONAL SOFTWARE SUPPORT MANUAL

IDENTIFICATION: 69-14-08A

DATE ISSUED 22 September 1967

SUBJECT: DEVICES AND OPERATIONAL LABELS
IN SIGMA 7 BCM

SECURITY

PAGE 6 OF 6

IV. Example of Adding a Device

The following update cards will provide for another card reader whose device address is X'06'. The line numbers correspond to the "B" version listing (Cat. 704133), Revision B00 (7/12/67).

+95	CRB	EQU	X'06'	ESTABLISH SYMBOLIC DEVICE ADDRESS
+639		DATA,2	CRB	ENTER DEVICE ADDRESS IN DCT1
+662		DATA,1	X'40'	ENTER FUNCTION (read only) into DCT3
+677		DATA,1	4	ENTER DEVICE "TYPE" INTO DCT4
+694		DATA,2	DA(A45)	ENTER ADDRESS OF COMMAND PAIR INTO DCT9
+805	A45	GEN,8,24	2,0	ESTABLISH COMMAND LIST FOR CRA06
		GEN,8,24	X'1E',0	

SIGMA 7 BASIC CONTROL MONITOR MODIFICATIONS

Additional Processing Capabilities

I. Basic Control Monitor

A. The following control commands have been added:*

!REW dcb

The device associated with the dcb is rewound if the device is mag tape, otherwise this control command is ignored.

!WEOF dcb

- (1) dcb device = mag tape – tape mark is written
- (2) dcb device = paper tape punch – !EOD record is written
- (3) dcb device = card punch – !EOD record is written
- (4) dcb device = any other – ignored

!PFIL dcb [, BACK] [, N]

The device associated with dcb is positioned N files (backward or forward) if the device is mag tape, otherwise this control command is ignored.

Note: The default case is forward one file.

B. The following I/O function calls have been added:

1. Rewind (REW) and Write Tape Mark (WEOF) calls

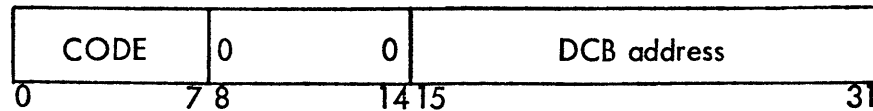
CAL, 1 address

where

address points to word 0 of the FPT shown below.

* Consult the SIGMA 7 Basic Control Monitor Manuals (90 09 63A), page 19, for a definition of 'dcb'.

word 0



where

CODE is X'01' for REWIND, and X'02' for WEOF.**

DCB address is the address of the associated DCB.

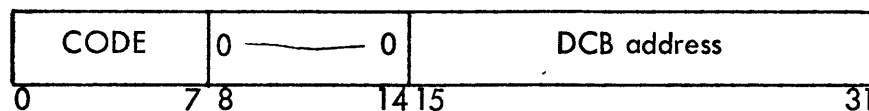
2. File and Record positioning calls

CAL1, 1 address

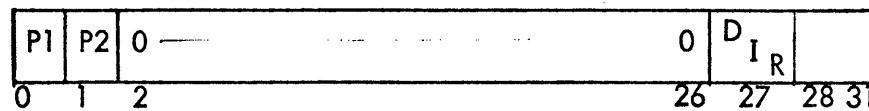
where

address points to word 0 of the FPT shown below.

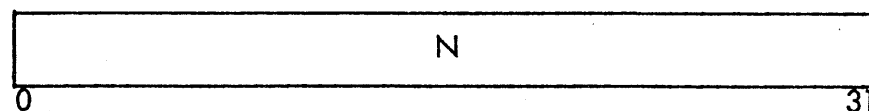
word 0



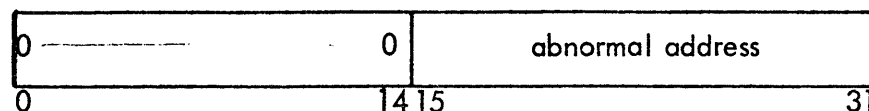
word 1



optional



optional



**If the device associated with the dcb is mag tape, a tape mark is written. If the device is a card or paper tape punch, an IEOD record is written.

where

CODE is X'1C' for position file, and X'1D' for position record.

DCB address is the address of the associated DCB.

*P1 is the record count (N) address parameter presence indicator (0 means absent, 1 means present) P1 must be 0 for position file.

P2 is the abnormal address parameter presence indicator (0 means absent, 1 means present) P2 must be 0 for position file.

DIR is the direction indicator (0 means forward positioning, 1 means backward positioning).

N is the number of records to position.

Abnormal address is the address of the entry to the users routine that will handle abnormal conditions for this I/O operation (position record only).

Note: Each of the I/O function calls described in 1 and 2 pertain only to magnetic tape and will be ignored if not applicable.

C. Changes to File Maintenance call philosophy:

1. Close File

The close file function (code X'15') has been modified to do the following:

- a. An IEOD record will be written if the device associated with the DCB is either the card punch or paper tape punch.
- b. Two successive tape marks (EOF's) are written followed by a backspace if the device associated with the DCB is mag tape (out file).

The implications of the close file changes are as follows:

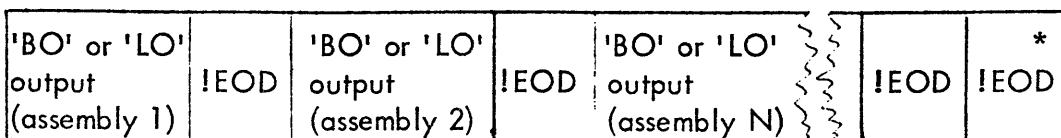
Assembly or Compilation

'BO' and 'LO' mag tapes

'BO' or 'LO' output (assembly 1)	E O F	'BO' or 'LO' output (assembly 2)	E O F	⋮	'BO' or 'LO' output (assembly N)	E O F	E O F
----------------------------------------	-------------	----------------------------------------	-------------	---	----------------------------------------	-------------	-------------

*If the count address parameter is 0 the default case for N is 1.

'BO' and 'LO' paper tapes



II. BCM Loader and BCM Abs Dump Loader

A. Load and Library phase

The loader reads the first record from the BI or LI device and examines it for two possibilities:

1. The first read resulted in an End of Data return.
2. The first read resulted in a normal return.

If the End of Data return occurs (EOF on mag tape), the BI device is rewound, another read issued, and loading takes place.

If the normal return occurs, the image is accepted and loading takes place.

This change allows semibatch processing capabilities inasmuch as binary output to mag tape can be loaded as part of the same assembly compilation job. I. e.,

```
!JOB
!ABS (PROCESSOR)
    processor abs deck
!ASSIGN M:BO, (DEVICE, MTA80)
!Processor BO
    deck 1
!Processor BO
    deck 2
    :
!Processor BO
    deck N
!ABS LOADER
    loader abs deck
!ASSIGN M:BI, (DEVICE, MTA80)
!ASSIGN M:LI, (DEVICE, CRA03)
```

*Second !EOD record can be generated by the !WEOF $\left\{ \begin{matrix} M:BO \\ M:LO \end{matrix} \right\}$ control command.

!LOAD (MAP)
library decks
!EOD
!EOD
!DATA
!RUN
!FIN

Note: The loaders (either F. S. or BCM versions) terminate the load phase upon encountering 2 successive End of Data returns (!EOD, !EOD for paper tape or cards; EOF EOF for mag tape). An end transfer address will no longer cause the load phase to terminate.

The same 2 successive End of Data returns apply to the termination of the library phase with the exception that after all external references are satisfied the remainder of LI is read but not loaded.

III. BCM and Free-Standing SYMBOL Assembler

Both versions of the SYMBOL Assembler will now assemble multifile source tapes as follows:

An End of Data return from the first read of any assembly causes another read to be made. If the second read results in a normal return, assembly begins. If the second read results in an End of Data return the following message is written on the OC device:

2 SUCCESSIVE EOF'S READ... EOT

BCM SYMBOL then exits to the monitor.

F. S. SYMBOL enters a "wait" state. (Clearing the wait results in a restart.)